

Notice of References Cited

Application/Control No.

10/749,412

Applicant(s)/Patent Under
Reexamination
OCHI ET AL.

Examiner

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Art Unit

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2001/0053220 A1	12-2001	Kocher et al.	380/29
*	B	US-2004/0047466 A1	03-2004	Feldman et al.	380/037
*	C	US-6,937,727 B2	08-2005	Yup et al.	380/37
*	D	US-7,043,016 B2	05-2006	Roelse, Petrus Lambertus Adrianus	380/29
*	E	US-7,194,090 B2	03-2007	Muratani et al.	380/29
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	GB 2345229 A	06-2000	UK	Bo Lin , Stephen McSpadde	
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Henry Kuo and Ingrid Verbauwhede, "Architectural Optimization for a 1.82Gbits/sec VLSI Implementation of the AES Rijndael Algorithm", Springer-Verlag, LNCS 2162, pp. 51-64, 2001.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.